

Application No. 10/707,546
Amendment due February 14, 2006
Reply to Office Action of November 14, 2005

Docket No.: 22040-00025-US1

AMENDMENTS TO THE SPECIFICATION

Please replace the TITLE with the following new TITLE:

INTEGRATED CIRCUIT WITH REDUCED ANALOG COUPLING NOISE

Please replace paragraph [0017] with the following replacement paragraph:

[0017] In particular, in recent years, an analog/digital mixed LSI has been used in many cases, which is equipped with an analog circuit block and ~~distal~~digital circuit block as a plurality of circuit blocks integrated on a single chip. In such analog/digital mixed LSI, when analog control lines are wired in the upper layer or lower layer of digital circuit block, digital signals with large voltage, such as clock signals, overlap minute analog control signals via parasitic capacity. This has tended to lead to a problem causing erroneous operation.

Please replace paragraph [0053] with the following replacement paragraph:

[0053] In the IC chip 20, which integrates the AM receiving function and FM receiving function in a single chip, when receiving the AM radio broadcasting, the AM circuit block 1 is ~~tuned~~turned ON and the FM circuit block 2 is turned OFF. Additionally, when receiving the FM radio broadcasting, the AM circuit block 1 is turned OFF and the FM circuit block 2 is tuned ON. Thus, neither AM circuit block 1 nor FM circuit block 2 is ~~tuned~~turned ON simultaneously.

Please replace paragraph [0058] with the following replacement paragraph:

[0058] Moreover, in the aforementioned embodiments, the wiring layout of the analog control lines is explained. However, the present invention is applicable to the wiring layout of an analog signal line in the same manner. An example of a circuit where the feedback loop shown in FIG. 6 is arranged is assumed. The circuit shown in FIG. 6 is equipped with an adder 51 and an analog circuit 52 that performs processes ~~ef~~on the analog signal outputted from the adder 51, and feedback input of the output signal of the analog circuit 52 is ~~executed~~provided to the adder 51 via the analog signal line 53.